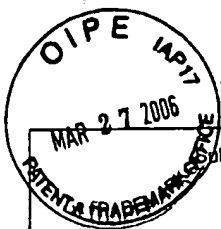


Form PTO-1449			ATTORNEY DOCKET NO. AUS920040065US1		SERIAL NO. 10/806,866
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary) JUN 29 2005 PATENT & TRADEMARK OFFICE			APPLICANT Dimpsey et al.		
			FILING DATE March 22, 2004		GROUP ART UNIT 2465 2185
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
A.S.	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989
	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999
	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000
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LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT Dimpsey et al.			
			FILING DATE March 22, 2004		GROUP ART UNIT 2165 2185	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME		CLASS/ SUBCLASS	FILING DATE
A.S.	BB	2002/0129309	Sep. 12, 2002		Floyd et al.	714/724 Dec. 18, 2000
	BC	2001/0032305	Oct. 18, 2001		Barry	712/34 Feb. 23, 2001
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY		CLASS/ SUBCLASS	TRANSLATION YES NO
A.S.	BH	JP2000029731	Dec. 8, 1999		Japan	G06F 9/38 X
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A.S.	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug			Nov. 4, 1999
A.S.	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System			Oct. 2, 1995
DATE CONSIDERED 9/29/06		EXAMINER <i>[Signature]</i>				
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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		ATTORNEY DOCKET NO. AUS920040065US1	SERIAL NO. 10/806,866		
APPLICANT Dimpsey et al.					
		FILING DATE March 22, 2004	GROUP ART UNIT 2165 2185		
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL	CLASS	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
A.S.	BR	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
	BS	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
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	BU	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
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	BX	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
	BY	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
	BZ	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
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	CB	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
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A.S.	CE	10/757,192 / AUS920030548US1	DeWitt, Jr. et al.	Method and Apparatus for Determining Computer Program Flows Autonomically Using Hardware Assisted Thread Stack Tracking and Cataloged Symbolic Data	March 18, 2004
DATE CONSIDERED 9/29/06			EXAMINER <i>[Signature]</i>		
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LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Dimpsey et al.	
				FILING DATE March 22, 2004	
		GROUP ART UNIT 2466 2185			
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
A.S.	CF	10/757,227 / AUS920030555US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Moving Cache Entries to Dedicated Storage when False Cache Line Sharing is Detected	Jan. 14, 2004
	CG	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004
	CH	10/807,094 / AUS920040059US1	Dimpsey et al.	Method and Apparatus for Providing Hardware Assistance for Code Coverage	Mar. 22, 2004
	CI	10/808,716 / AUS920040060US1	Dimpsey et al.	Method and Apparatus for Autonomic Test Case Feedback Using Hardware Assistance for Code Coverage	Mar. 22, 2004
	CJ	10/806,576 / AUS920040061US1	Dimpsey et al.	Method and Apparatus for Hardware Assistance for Data Access Coverage	Mar. 22, 2004
	CK	10/806,633 / AUS920040062US1	Dimpsey et al.	Method and Apparatus for Providing Hardware Assistance for Data Access Coverage on Dynamically Allocated Data	Mar. 22, 2004
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DATE CONSIDERED 9/29/06			EXAMINER <i>[Signature]</i>		
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Complete if Known

Application Number	10/806,866
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Popovich, Don Arpan Sark
Art Unit	2165 2185
Attorney Docket No.	AUS920040065US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
A.S.	AA 5,710,881	01-20-1998	Gupta et al.	
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NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
A.S.	AH TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
A.S.	AI TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
A.S.	AJ ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

Examiner:

Arpan Sark

Date Considered:

9/29/06

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/806,866
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Popovici, Dor Arpan Savla
Art Unit	2165 2185
Attorney Docket No.	AUS920040065US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
A.S.	AA 5,797,019	08-18-1998	Levine et al.	
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A.S.	AG Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Application Number 10/806,866
 Filing Date 03-22-2004
 First Named Inventor Dimpsey et al.
 Examiner Name Popovici, Dov Arpa Savla
 Art. Unit 2165 2185
 Attorney Docket No. AUS920040065US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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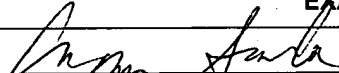
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10806866	
	Filing Date		2004-03-22	
	First Named Inventor		Dimpsey et al.	
	Art Unit		2165-2185	
	Examiner Name		Gaffin, Jeffrey A. Arpan Sarik	
	Attorney Docket Number		AUS920040065US1	

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A.S.	2	6721875	B1	2004-04-13	McCormick et al.		
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	Filing Date		2004-03-22
	First Named Inventor	Dimpsey et al.	
	Art Unit	2165 2185	
	Examiner Name	Gaffin, Jeffrey A Arpan Jarla	
Attorney Docket Number		AUS920040065US1	

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